

Form PTO 1449
(Modified)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY DOCKET NO.
210793US2SRDSERIAL NO.
New Application

LIST OF REFERENCES CITED BY APPLICANT

APPLICANT

Shoji OTAKA

FILING DATE
HerewithGROUP
2642

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO	
	AO					
	AP					
	AQ					
	AR					
	AS					
	AT					
	AU					
	AV					

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

HH	AW	Jan CROLS, et al., "A Single-Chip 900 MHz CMOS Receiver Front-End with a High Performance Low-IF Topology", IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol. 30, No. 12, pages 1483-1492, December 1995
	AX	
	AY	
	AZ	

Examiner

HONG

Date Considered 3-7-05

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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